



DEPARTMENT OF ELECTRONICS

St. Joseph's College (Autonomous)

SPECIAL HERITAGE STATUS AWARDED BY UGC, ACCREDITED AT 'A++' GRADE (CYCLE-IV) BY NAAC
COLLEGE WITH POTENTIAL FOR EXCELLENCE BY UGC, DBT-STAR & DST-FIST SPONSORED COLLEGE

TIRUCHIRAPPALLI- 620 002, INDIA

ORGANIZES

TWO DAYS INTERNATIONAL WEBINAR ON

**"Battery less Low power Edge AI on Earth
and Beyond"**

&

**"Navigating the Future: what's in store for
Electronics Engineers"**

DATE: 29-01-2025 & 30-01-2025

TIME: 2.00 PM

FELICITATION BY

Rev. Dr. S. Mariadoss SJ

Principal, St. Joseph's College, Trichy

RESOURCE PERSONS

Dr. J. Anuj

Engineering Researcher at imec-IDLab
BELGIUM

Mr. Cyril Anand

CEO- STAR LINK
QATAR

WELCOME ADDRESS

Dr. B. Kanickairaj

HEAD

DEPT. OF ELECTRONICS

VOTE OF THANKS

Dr. J. Gobinath

ASSISTANT PROFESSOR

DEPT. OF ELECTRONICS

FACULTY AND STUDENTS

ALL ARE INVITED